	ISSUE CLASSIFICATION	
Class	Subclass	

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PATENT NUMBER

U.S. REISSUE Patent Application

	O.I.P.E.	O.G. PUBLICATION DATE	REISSUE PATENT DATE
	SCANNED TR2 Q.A. Am		

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
10/084757	D	395		2782	D. Chang
		326	38,	2819	

APPLICANTS

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TITLE

System for coupling programmable logic device to external circuitry
which selects a logic standard and uses buffers to modify output and
input signals accordingly

PTO-2040
12/99

SURRENDER OF ORIGINAL PATENT _____ (Exr. Initials) ORIGINAL PATENT NUMBER _____

ISSUING CLASSIFICATION

CROSS REFERENCE(S)

ORIGINAL		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
CLASS	SUBCLASS		

INTERNATIONAL CLASSIFICATION

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
				Amount Due	Date Paid
☐ The terminal _____ months of this patent have been disclaimed.	(Primary Examiner) (Date)			ISSUE BATCH NUMBER	
FINAL SPRE REVIEW					
_____ (INITIALS)	(Legal Instruments Examiner) (Date)				

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